



DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING
GAYATRI VIDYA PARISHAD COLLEGE FOR DEGREE & P.G. COURSES (A)
RUSHIKONDA, VISAKHAPATANAM 530045.
(Approved by A.I.C.T.E, Affiliated to Andhra University)
An ISO 9001:2015 Certified Institute.

Technical Achievements of Students

Academic Year: 2018-2019

S.No.	Organizing Body	Event	Name	Date	Class	Batch
1.	MAVEN SILICON	RTL Design & Functional Verification	V Pavan Kumar	29-06-18	III/IV	2016-20
2.	MAVEN SILICON	RTL Design & Functional Verification	P Srikanya	29-06-18	III/IV	2016-20
3.	MAVEN SILICON	RTL Design & Functional Verification	D Dev Abhishek	29-06-18	III/IV	2016-20
4.	MAVEN SILICON	RTL Design & Functional Verification	M Yaswanth	29-06-18	III/IV	2016-20
5.	MAVEN SILICON	RTL Design & Functional Verification	D Sri Lakshmi	29-06-18	III/IV	2016-20
6.	MAVEN SILICON	RTL Design & Functional Verification	P Ambika	29-06-18	III/IV	2016-20

7.	MAVEN SILICON	RTL Design & Functional Verification	KGNV Laxmi	29-06-18	III/IV	2016-20
8.	MAVEN SILICON	RTL Design & Functional Verification	MS Sirichandana	29-06-18	III/IV	2016-20
9.	MAVEN SILICON	RTL Design & Functional Verification	T Mounika	29-06-18	III/IV	2016-20
10.	MAVEN SILICON	RTL Design & Functional Verification	B Anusha	29-06-18	III/IV	2016-20
11.	MAVEN SILICON	RTL Design & Functional Verification	M Lavanya	29-06-18	III/IV	2016-20
12.	MAVEN SILICON	RTL Design & Functional Verification	S Prathyusha	29-06-18	III/IV	2016-20
13.	MAVEN SILICON	RTL Design & Functional Verification	V Sanath Kalyan	29-06-18	III/IV	2016-20
14.	MAVEN SILICON	RTL Design & Functional Verification	PHSSRS Sai Kumar	29-06-18	III/IV	2016-20
15.	MAVEN SILICON	RTL Design & Functional Verification	TVSL Tejaswy	29-06-18	III/IV	2016-20
16.	MAVEN	RTL Design	Akhil Prasad	29-06-18	III/IV	2016-20

	SILICON	& Functional Verification	Choudhory			
17.	MAVEN SILICON	RTL Design & Functional Verification	G Pavani	29-06-18	III/IV	2016-20
18.	MAVEN SILICON	RTL Design & Functional Verification	B Roshini	29-06-18	III/IV	2016-20
19.	MAVEN SILICON	RTL Design & Functional Verification	V Nalini	29-06-18	III/IV	2016-20
20.	MAVEN SILICON	RTL Design & Functional Verification	K Varshini	29-06-18	III/IV	2016-20
21.	MAVEN SILICON	RTL Design & Functional Verification	M Nithisha	29-06-18	III/IV	2016-20
22.	MAVEN SILICON	RTL Design & Functional Verification	DS Sanyasi Rao	29-06-18	III/IV	2016-20
23.	I3indya Technologies in assoc. with IIT Bombay	IoT Challenge 2019	V Pavan Kumar	29-09-18 to 30-09- 18	III/IV	2016-20