CURRICULUM VITAE

1. **NAME OF THE APPLICANT** : Ch. Krishna Chaitanya Sai

2. **DATE OF BIRTH** : 24-08-1997

3. **ADDRESS FOR COMMUNICATION** : Assistant Professor
   GVPCDPGC
   Visakhapatnam
   530027

4. **E-MAIL ADDRESS** krishnachaitanyach4@gmail.com

5. EDUCATIONAL QUALIFICATIONS

Qualification	Specialization	Institute/University	Year of Pass	Class
SSC	General	ADAMS High School	2012	First
Intermediate	MPC	NARAYANA Junior College, Visakhapatnam	2014	Distinction
B.Tech.	ECE	ANITS	2018	First
M.Tech.	VLSI	ANDHRA UNIVERSITY	2021	Distinction
Ph.D	ML&VLSI	JNTU-K		

6. WORK EXPERIENCE

Designation	Organization	Period
Assistant Professor	Visakha Institute of Engineering and Technology	01/07/2022 to 11/09/2024
Assistant Professor	Gayatri vidya parishad college for degree and pg course	12/09/2024 to till date

7. ACADEMIC PROJECT

- Design and implementation of 64-bit signed multipliers.
  Objective: To implement 64 bit signed multipliers using verilog HDL in Xilinx.
- Driver drowsiness detection and accident avoidance system.
  Objective: To prevent accidents using IR sensor and ultrasonic sensor.

9. INTERNSHIP

- Completed an internship at Airport authority of India, Visakhapatnam.

  Project Title: Study of Communication and Air Navigation Systems.

10. RESEARCH WORK

- Ambarapu Sudhakar, Popuri Rajani Kumari, Cheepurupalli Krishna Chaitanya Sai "Reduction of torque ripples using the DTC-SVM method in PMSM with extended Kalman filter"IAES,VOLUME 13 NO 4, December2024.
- Cheepurupalli Krishna Chaitanya, Pillla Likhitha Sri, Ellapu Bhanu Prakash "PICK AND PLACE ROBOTIC ARMS MOVEMENT CONTROLLED BY ANDROID WIRELESSLY", IJRASET, Volume 11 Issue X Oct 2023,ISSN:2321-9653.
- Cheepurupalli Krishna Chaitanya, Pilla Likhitha Sri, Ellapu Yagna Varahala Rao," AN ARDUINO BASED ALCOHOL, DROWSINESS AND GPS DETECTION" IJCRT ,Volume 12, Issue 6 June 2024 ISSN: 2320-2882.
- Kalla Sandhya, Cheepurupalli Krishna Chaitanya "DESIGN AND HIGH PERFORMANCE EVALUATION OF A LOW POWER BIT-LINE SRAM PMOS BIASED SENSE AMPLIFIER", IJSREM,Volume 08,Issue 06, June – 2024,ISSN: 2582-3930 .

11. EXTRACIRCULAM

- Qualified in **APRCET-2024(RANK-50)**
- Worked on NAAC criteria-1,criteria-2
- Worked on NBA
- Participated in multipe no.of FDP's

11. SKILLS

- Digital and Analog electronics,
- VHDL,Verilog HDL,
- Xilinx,
- Ltspice
- Basic C,
- Basic Python.

Declaration:I hereby declare that all the particulars furnished above are genuine to the best of my knowledge.

Date:

Place: GVPCDPGC

(Ch. Krishna Chaitanya Sai)