

CURRICULUM VITAE

RAJANA KANAKA RAJU

M.Sc , M.Tech. (Ph.D)

Door No: 18-121/A/B,
Street No-4, Adithya Nagar,
Near Sri Venkateswara Swamy Temple,
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OBJECTIVE :

To work for an organization, where I can apply my technical proficiency in working towards the organizational needs and benefits, and to traditionally render professional services for the organizational and personal growth.

SUMMARY

- Masters in Electronics M.Sc.(Electronics) and Masters in Computer Science (M.Tech) Specialization in Artificial Intelligence and Robotics (AI&R) with 22 years of Teaching experience in which 16 years as Assistant Professor & 6 years as Associate Professor.
- Member in the Indian Science Congress Association (ISCA), Kolkata.
- Active member and played a lead role in many cultural events.
- Actively participated and presented a paper on “Study on Neural Networks” in 95th Indian Science Congress Association (ISCA) held in Visakhapatnam during 3rd – 7th Jan 2008.
- Practical Examiner for B.E/B.Tech Computer Science Engineering and Information Technology.

WORK EXPERIENCE

Working as an Assistant Professor in the Department of Computer Science Engineering Gayatri Vidya Parishad College for Degree & PG Courses(A), Visakhapatnam From 15th December 2022 onwards.

Working as an Senior Assistant Professor in the Department of Computer Science Gayatri Vidya Parishad College for Degree & PG Courses(A), Visakhapatnam From 2nd July 2014 onwards.

Worked as Associate Professor in the Department of Information Technology, Raghu Institute of Technology, Dakamarri, Visakhapatnam from 26th August 2008 to 30th May 2014.

Worked as Assistant Professor in the Department of Electronics G.V.P. Degree & PG College, Visakhapatnam from 12th September 2001 to 21st August 2008.

EDUCATIONAL QUALIFICATION

Education	Institute	Year of passing	Percentage
Ph.D (Pursuing) in Computer science and Engineering	GITAM UNIVERSITY	PRE Ph.D COMPLETED WITH CGPA 8.50	
M.Tech(CST) Specialization in AI & R	Andhra University College of engineering, Andhra University, Visakhapatnam.	2008	74.45%
M.Sc(Electronics)	G.V.P College for PG courses, Visakhapatnam	2001	69.03%
B.Sc(MPE)	G.V.P Degree College, Visakhapatnam	1999	70.02%
Intermediate	IPE Board	1996	68.00%
Matriculation	Andhra University	1994	57.00%

RESEARCH ACTIVITIES

- ❖ Participated in 95th ISCA (Indian Science Congress Association) held in the month of Jan 2008 at Andhra University, Visakhapatnam
- ❖ Recognized as a co-author for a book titled by Fundamentals Of Computers published by Himalaya Publishing

- ❖ Acted as a Mentor in the Nation-wide Entrepreneurship challenge HUNAR 2.0 – IN SEARCH OF GEN Y ENTREPRENEURS organized BY jaro Education in association with IFEEEL Institute , Lonavala held in the year 2018-19
- ❖ Paper published on Pumping lemma for Regular and Context free Languages and Ogden's Lemma for Context free Languages International Journal of Computer Applications (0975 – 8887) Volume 39– No.10,February 2012
- ❖ Awarded **Elite** NPTEL Online Certification for successfully completing the course **Computer Architecture and Organisation**
- ❖ Awarded **Elite + Silver** NPTEL Online Certification for successfully completing the course **Introduction to Research** with a consolidated score of 84% examination held in the month of Aug -Oct 2022
- ❖ Awarded NPTEL Online Certification for successfully completing the course **Social Networks**
- ❖ Awarded **Elite+Silver** NPTEL Online Certification for successfully completing the course **Research Ethics** with a consolidated score of 86% examination held in the month of February 2023.
- ❖ Paper published on Pumping lemma for Regular and Context free Languages and Ogden's Lemma for Context free Languages International Journal of Computer Applications (0975 – 8887) Volume 39– No.10,February 2012
- ❖ Paper published on Tea algorithm based industrial automation system using xbee's International Journal of Computer Science and Engineering Volume 6, Issue-7, July 2018.
- ❖ Paper published on Intelligent Traffic Monitoring System by Using ZIGBEE and VANET Technology International Journal of Computer Science and Engineering Volume 8, Issue-7,July 2020.
- ❖ Paper published on An Image Encryption Technique Based on Logistic Sine Map and an Encrypted Image Retrieval Using DCT Frequency,
Recent Trends in Intensive Computing
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This article is published online with Open Access by IOS Press and distributed under the terms of the Creative Commons Attribution Non-Commercial License 4.0 (CC BY-NC 4.0).doi:10.3233/APC210171
- ❖ Successfully completed the 5-day Online FDP on the theme “Inculcating Universal Human Values in Technical Education” organized by All India Council for Technical Education (AICTE) from 3rd April to 7th April 2023.
- ❖ Paper published on A NOVEL IMPLEMENTATION OF PEDESTRIAN DETECTION USING HOGD AND SVM ALGORITHMS The Journal of Computational Science and Engineering. ISSN: 2583-9055 Volume:1, Issue: 2 September 2023

- ❖ Successfully completed the one week Online International Workshop on “Introduction to Machine Learning and Deep Learning (Hands-on)” organized by Department of CSE In Association with Directorate of Faculty Development Cell, JNTUGV from 31 July -05 August 2023.
- ❖ Successfully completed the NEP 2020 Orientation & Sensitization Programme under Malaviya Mission Teacher Training Programme (MM-TTP) of University Grants Commision (UGC) Organized by UGC-MMTTC,Andhra University,Visakhapatnam from 05-02-2024 to 14-02-2024.
- ❖ Awarded **Elite + Silver** NPTEL Online Certification for successfully completing the course **Introduction to Internet of Things** with a consolidated score of 84% examination held in the month of Jul -Oct 2023.
- ❖ Awarded **Elite + Gold** NPTEL Online Certification for successfully completing the course **Foundation of Cloud IoT Edge ML** with a consolidated score of 90% examination held in the month of Feb-Apr 2024.
- ❖ TWO-Day National Conference on "Computational Intelligence and Sustainable Systems [NCCISS-2024] on 13th - 14th April 2024.Presented a paper entitled "A Comprehensive Analysis of Lucas Numbers and Their Flexibility in Trimetric Graph Optimization for Revealing Latent Relations" held at GVPCDPGC(A),Visakhapatnam,A.P,India.
- ❖ Expert in Big Data ,Hive using Hadoop installation using shell script
- ❖ **Membership Certificate** :This is to recognize you as a life member of The Journal of Computational Science and Engineering [ISSN: 2583-9055] with a membership ID: LM_JCSE_005
- ❖ **Membership Certificate** :This is to recognize you as a life member of The Indgiants Association of Educators & Professional Trainers. This membership is awarded for his significant contributions towards the association and the field of education.
- ❖ **Membership Certificate** :This is to certify that Rajana Kanaka Raju having fulfilled the requirements for Professional Membership has been admitted as a member of Association for Computing Machinery

❖ **PATENT TITLE:**

“SYSTEM AND METHOD FOR SECURE COMPUTATION THROUGH SECRET SHARING AND ZERO-KNOWLEDGE PROOFS”

Application Number:202441005167

Date of Filing:25-01-2024

Date of Publication:09-02-2024

SEMINARS/WORKSHOPS

S. No.	Date	Venue	Title
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1	21 st April 2006	Andhra University Visakhapatnam	Mixed Signal Processing & Recent Advances in Electronics
2	3 rd Feb 2007	Andhra University Visakhapatnam	Low – Power VLSI Design & Technology
3	12 th April 2008	G.V.P Degree & Pg courses Visakhapatnam	Embedded Systems & Digital Signal Processing
4	28 th to 30 th October 2010	G.V.P.college of Engg, Madurawada,VSP	Interfacing Through Microprocessors
5	2 nd to 4 th Jan 2012	JNTUK,kakinada	Proven Strategies for Effective Teaching
6	29 th - 30 th November 2018	G V P College For Degree And Pg Courses (A)	IBM Rational Software Architect Designer
7.	16 th November 2019	G.V.P.college of Engg, Madurawada,VSP	RPA Explorer in association with UiPath
8.	20th July 2023	P.B.Siddhartha College of Arts & Science ,Vijayawada	QAC - APSCHE “Specialised Skilling Solution”

SUBJECTS TAUGHT

- ☐ Systems Programming
- ☐ Operating Systems (Theory and lab) for B.Tech. and M.Tech
- ☐ Computer Networks
- ☐ Data Structures using C,CPP and JAVA (Theory and lab)
- ☐ C Programming in Numerical Methods(Theory and lab)
- ☐ Software Engineering
- ☐ Microprocessor-I & II(Theory and lab)
- ☐ Digital Logic Design and Microprocessor
- ☐ Computer Organization
- ☐ Big Data Analytics
- ☐ Internet of Things

PROJECTS GUIDED

- ☐ "ENT EXPERT SYSTEM " is done as a part of www.bharatgramarogya.net.
- ☐ Traffic Light Interface Using 8085 microprocessor kit mps-85.
- ☐ Stepper Motor Interface Using 8085 microprocessor kit mps-85..
- ☐ An efficient approach for traffic detection
- ☐ An efficient algorithm for securing smart cards
- ☐ An approach for restructuring of web pages

OTHER RESPONSIBILITIES

- ☐ In-charge H.O.D in the Dept. of Information Technology.
- ☐ Lab In-charge for Dept. of Information Technology.
- ☐ In-charge Training Placement Officer

PERSONAL DETAILS :

Name	Rajana Kanaka Raju
Father Name	Rajana Venkata Ramana
Date of Birth	28-02-1979
Gender	Male
Marital Status	Married
Present Address	D.NO:- 48-5-49 (Upstairs), Srinagar, Visakhapatnam-530016.
Nationality	INDIAN
Languages known	English,Telugu
Strengths	Hardworking and positive thinking

DECLARATION :

I hereby declare that all the statements made in the above application are correct to the best of my knowledge and belief.

Date:

Station: Visakhapatnam

(R. KANAKA RAJU)

