

**Dr. D.S.S.N Raju**  
M.Sc. Tech VLSI Design  
Andhra University, Visakhapatnam  
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**Career Objective:** To be associated with an organization where my academic and research experience can be utilized for the development of the organization as well as improve to my inter personnel skills.

**ACADEMIC DETAILS:**

| QUALIFICATION   | INSTITUTE   | PERCENTAGE/ GRADE | YEAR |
|---|---|-------------------|------|
| Ph.D. in Electronics  | Andhra University   | -----             | 2021 |
| M.Sc. (Tech) VLSI Design  | Andhra University,<br>Visakhapatnam                               | 7.98 CGPA         | 2011 |
| Advanced Certificate Training Program in ASIC Design (Synthesis, Implementation and Verification) | M.S. Ramaiah School of Advanced Studies, Bangalore                | B                 | 2011 |
| Certificate Course on Hardware & Networking   | CIS Complete IT Solutions, Kakinada.                              | A                 | 2008 |
| B. Sc (M.P.C)   | Modern Degree College, R.C.P.M (Affiliated to A.U, Visakhapatnam) | 57.63%            | 2008 |
| Intermediate  | D.R Jr. College, Kolanka.   | 51.7%             | 2004 |
| SSC   | S.D.V.R.R.A. High School Kolanka.                                 | 58%               | 2002 |

**Ph.D. Thesis Title:****DEVELOPMENT OF AN ALGORITHM SUITE OF ENERGY EFFICIENT ROUTING PROTOCOLS USED IN WIRELESS SENSOR NETWORKS****TEACHING EXPERIENCE:**

- Worked as a Guest Faculty in the Department of Physics, Andhra University Visakhapatnam
- Working as an Assistant professor in the Department of Physics, Gayatri Vidya Parishad College for Degree and P.G Courses (Autonomous).

**SUBJECTS HANDLED:**

- Linear Integrated Circuits
- Signals and Systems
- Analog and Digital Electronics
- Microprocessor and Microcontroller
- Electricity, Magnetism and Electronics
- Waves and Oscillations
- Wave optics
- Low Power VLSI Design
- Power Electronics
- ASIC Design
- FPGA Design
- Computer Organization and Architecture
- VHDL/Verilog
- Applied Mathematics
- C Language
- Oracle

**KEY SKILLS:**

- **Programming Languages:** C, Verilog, VHDL, System Verilog, MATLAB, Oracle, Python
- **Operating Systems:** Linux, Windows XP, Vista, windows7, windows8, windows 10
- **Content Editing:** Microsoft office suite, HTML

**Memberships:**

Associate Member in Institution of Engineers (India) 8 Gokhale Road, Kolkata.

#### **INTERNATIONAL PUBLICATIONS:**

1. D.S.S.N. Raju (2018),” Multipath Cluster based Hybrid MAC Protocol for Wireless Sensor Network”, ***International Journal of Management, Technology and Engineering*** Volume IX, Issue I, p4095-4017, JANUARY/2019
2. D.S.S.N. Raju (2018),” Mobile agent PSO-GA based Energy Efficient Routing Protocol in WSN”, ***International Journal of Research in Electronics and Computer Engineering*** Vol. 6 Issue 2, p1145-1150, Apr.-June 2018
3. D.S.S.N. Raju (2017),” Determination of Channel Capacity Using Shannon's Relation Through Cerebellar Model Arithmetic Computer Using Artificial Intelligence”, ***IUP Journal of Computer Science*** Vol. 11 Issue 3, p44-52, Jul 2017.
4. D.S.S.N. Raju (2017),” PC-Based Real Time Data Processing System for Doppler Sodar Applications”, ***International Journal of Computer Sciences and Engineering*** Vol.-5, Issue-2 E-ISSN: 2347-2693
5. D.S.S.N. Raju (2016),” Development of Gray Scale Image Processing Computing Technique to Remove the Unwanted Frequency Patches on SODAR Facsimiles”, ***International Journal of Computer Sciences and Engineering*** Vol.-4(12), Dec 2016, E-ISSN: 2347-2693
6. D.S.S.N.Raju (2016), “Scattering of SODAR Signal Through Rough Circular Bodies”, ***Springer Telecommunications*** Volume 372 of the series Lecture Notes in Electrical Engineering pp 277-290
7. D.S.S.N. Raju (2014), “An Automatic Patient Severity Indication System Using Pattern Classification with Adeline Neural Network”, ***International Journal of Advanced Technology in Engineering and Science***, Vol.2, Special Issue 01, p.p:612-617 (ISSN (online): 2348 – 7550)
8. D.S.S.N.Raju (2014), “Scattering of Sodar Signal by Turbulence in Homogenous, Isotopic and other Mediums”, ***International Journal of Computer Science and Engineering***, Vol.2, No.4, p.p:1-5 (ISSN: 2347-2693[online])
9. D.S.S.N. Raju (2014), “A Novel Mathematical Computing Simulation Methodology for Acoustic Phased Array antenna of SODAR System”, ***International Journal of Current Engineering and Technology***, Vol.4, No.2, p.p:1064-1067(ISSN: 2277-4106[online], 2347-5161[print])
10. D.S.S.N. Raju (2013), “A Novel Method for Secured Assessment and Evaluation System in Education Paradigm”, ***International Journal of Innovative Research & Development***, Vol.No.2 (12) (Special Issue), p.p:113-114 (ISSN: 2278-0211[online], 2278-7631[print])

## PAPERS PRESENTED IN SEMINARS / CONFERENCES:

1. Presented a paper on “**GENERATION AND DECODING OF MULTI-FREQUENCY SIGNAL FOR SODAR APPLICATION**” in International Symposium Social Business for Sustainable Development, organized by Andhra University, Visakhapatnam.
2. Presented a paper on “Design and Simulation of Diversity Antenna Techniques using MATLAB as a SODAR Application”, **Research Scholars Congress-2015** Organized by Department of Electronics and Electrical Engineering, Indian Institute of Electrical Engineering, IIT Guwahati, Assam.
3. Presented a paper on “Design and Simulation of Diversity Antenna Techniques are using MATLAB”, in All India Seminar on **Telecommunication Switching Systems and Networks** Organized by IEI (India), Andhra University on 30 & 31 January’ 2014, p.p: 115-118.

## FDP’S Attended:

1. Participated & Completed successfully AICTE Training and Learning (ATAL) Academy Online Elementary FDP on "**Internet of Things**" from 23/08/2021 to 27/08/2021 at RAJAGOPAL POLYTECHNIC COLLEGE.
2. Participated & Completed successfully AICTE Training and Learning (ATAL) Academy Online Elementary FDP on "**Digital Forensics in Cyber Security**" from 02/08/2021 to 06/08/2021 at Centre for Development of Advanced Computing (C-DAC)
3. Participated & Completed successfully AICTE Training and Learning (ATAL) Academy Online Elementary FDP on "**Cyber Security and Incident Analysis: A Practical and Investigative approach** " from 19-07-2021 to 23-07-2021 at Andhra University College of Engineering (A).
4. Participated & Completed successfully AICTE Training and Learning (ATAL) Academy Online Elementary FDP on "**Cyber Security oversight for Information Protection**" from 12-07-2021 to 16-07-2021 at Cochin University of Science and Technology.
5. 5-Day FDP on Recent Trends in Applied Physics and Material Science, 8<sup>th</sup> -12<sup>th</sup> June 2020
6. FDP on MOODLE, 4<sup>th</sup> – 9<sup>th</sup> June 2020
7. National Online Faculty Development Programme on "Mentoring Skills, 1<sup>st</sup> - 3<sup>rd</sup> June 2020
8. Two Weeks Faculty Development Programme on Managing Online Classes and Co-Creating Moocs:2.0, 18<sup>th</sup> May – 3<sup>rd</sup> June 2020
9. One Week Online Faculty Development Programme on Techniques for Effective Teaching, 14<sup>th</sup> – 20<sup>th</sup> May 2020

10. Virtusa 2020 National Webinar Series on Transcending Lockdown Barriers Through Academic Outreach, 14<sup>th</sup> – 20<sup>th</sup> May 2020
11. Modern Methods for Teaching - Learning Practices, 12<sup>th</sup> – 13<sup>th</sup> May 2020
12. One-week FDP Programme on Arduino, 4<sup>th</sup> – 10<sup>th</sup> May 2020

**NATIONAL / INTERNATIONAL CONFERENCES / SEMINAR / SYMPOSIA / WORKSHOP/WEBNIARS/ ATTENDED**

1. Exploring the science of atmosphere from space, 26<sup>th</sup> august 2020
2. Exploration of physical science and engineering with Mathematica, 6<sup>th</sup> July 2020
3. Open-source tools & software's in remote teaching and learning, 5<sup>th</sup> July 2020
4. Yoga and health, 21<sup>st</sup> June 2020
5. Precautions of covid-19, 20<sup>th</sup> June 2020
6. National level online quiz on physics -2020, 15<sup>th</sup> June 2020
7. International webinar on post covid-19 education scenario: prospects and challenges, 6<sup>th</sup> June 2020
8. Emerging trends in nano-materials for microwave, integrated electronics and cancer applications, 4<sup>th</sup> June 2020
9. Creative writing of research proposals and articles, 4<sup>th</sup> June 2020
10. Introduction, applications and career in robotics, 25-05-2020 25<sup>th</sup> may 2020
11. Intellectual property rights (IPR), 15<sup>th</sup> may 2020
12. Online learning and its positive and negative impact on future generation education 14<sup>th</sup> May 2020
13. National workshop on "basics of r for life sciences" 28<sup>th</sup> -30<sup>th</sup> March 2019
14. National seminar on "emerging trends in materials science and technology" 9<sup>th</sup> - 10<sup>th</sup> March 2018
15. National workshop on "Electron Spin Resonance" 25<sup>th</sup> -27<sup>th</sup> December 2012
16. Certificate course on "embedded systems and applications" 6<sup>th</sup> -8<sup>th</sup> January 2012
17. National conference on "EMI/EMC/ems in design" 21<sup>st</sup> & 22<sup>nd</sup> December 2011
18. Certificate course on "Electrical and Electronics Laboratory Equipment Maintenance and Service" 23<sup>rd</sup> - 25<sup>th</sup> September 2011

19. Seminar on Semiconductor Technology Exploration Program (step 2k11) 8th – 10th August 2011
20. Student level national seminar on “Advances in Electronic Systems Design” 28th – 30th April 2011
21. National workshop – Cum training on “Cadence Eda Tools 23<sup>rd</sup> – 25<sup>th</sup> February 2011
22. National conference on “VLSI Design “26<sup>th</sup> &27<sup>th</sup> November 2010
23. National seminar on “Digital Signal Processing Using MATLAB Tools” 30<sup>th</sup> & 31<sup>st</sup> October 2010
24. National workshop on “Advanced Embedded System Design” 3<sup>rd</sup> & 4<sup>th</sup> September 2010
25. Workshop on “Embedded System Level Design” 17<sup>th</sup> April 2010
26. Workshop on “Low Power VLSI Design and Technology” 3<sup>rd</sup> February 2008
27. National seminar on “VLSI Technology” 10<sup>th</sup> -12<sup>th</sup> July 2008
28. National workshop on “Advanced Digital Signal Processing” 24<sup>th</sup> -26<sup>th</sup> July 2008
29. National seminar on “VLSI Signal Processing” 12<sup>th</sup> -14<sup>th</sup> August 2008
30. National workshop on “Communication Theory and Systems” 25<sup>th</sup> - 28<sup>th</sup> August 2008
31. National seminar on “VLSI DESIGN” 11<sup>th</sup> -13<sup>th</sup> September 2008
32. National seminar on “Microcontroller Architecture and Interfacing” 25<sup>th</sup> -27<sup>th</sup> September 2008
33. Workshop on “Mixed Signal Processing and recent advances in Electronics” 21<sup>st</sup> Dec 2008

**Personal Details:**

**Address:** S/O D.V.V.S.N. RAJU,  
2-132, KOLANKA, KAJULURU MANDAL,  
E.G. DIST, PIN-533464,  
Andhra Pradesh.

**Date of Birth:** 21-06-1987  
**Gender:** Male  
**Marital status:** Married  
**Nationality:** Indian  
**Language:** Telugu, English

I hereby promise that the above details given by me are true and correct to the best of my knowledge and belief.

D.S.S.N Raju